

### Application Data Sheet

#### **Application Information**

Application Type:: Regular  
Subject Matter:: Utility  
Title:: Cache Coherency In A  
Multi-Processor System  
Attorney Docket Number:: TI-35637 (1962-05421)  
Request for Early Publication?:: No  
Request for Non-Publication?:: No  
Suggested Drawing Figure:: 3  
Total Drawing Sheets:: 1  
Small Entity?:: No

#### **Applicant Information**

Applicant Authority type:: Inventor  
Primary Citizenship Country:: France  
Status:: Full Capacity  
Given Name:: Gerard  
Family Name:: CHAUVEL  
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Country of mailing address:: France  
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Applicant Authority type:: Inventor  
Primary Citizenship Country:: France  
Status:: Full Capacity  
Given Name:: Serge

Family Name:: LASSERRE  
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Applicant Authority type:: Inventor  
Primary Citizenship Country:: Finland  
Status:: Full Capacity  
Given Name:: Maija  
Family Name:: KUUSELA  
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Country of Residence:: France  
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Applicant Authority type:: Inventor  
Primary Citizenship Country:: Belgium  
Status:: Full Capacity  
Given Name:: Dominique  
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**Correspondence Information**

Correspondence Customer Number:: 23494

**Representative Information**

Representative Customer Number:: 23494

**DOMESTIC PRIORITY INFORMATION**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Provisional of	60/400,391	07/31/02

**FOREIGN PRIORITY INFORMATION**

Country::	Application number::	Filing Date::	Priority Claimed::
EPO	03291925.0	July 30, 2003	YES

**Assignee Information**

Assignee name:: Texas Instruments Incorporated  
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